

shown. The circuit comprises a plurality of video signal lines 211, 212, 213, a plurality of switch units 221, 222, 223, a buffer unit for inverting a scanning signal 231 and ~~an active area (display area)~~ a display area (an active area) 241. The plurality of video signal lines 211, 212, 213 supply analog video signals; for example, the video signal line 211 supplies an analog video signal for blue, the video signal line 212 supplies an analog video signal for red, and the video signal line 213 supplies an analog video signal for green.

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In this embodiment, the plurality of switch units 221, 222, 223 can be of any electronic switches, and preferably, transistors, and more preferably, thin film transistors (TFTs). The plurality of switch units 221, 222, 223 are disposed between the video signal line 212 and the video signal line 213. Each n-type control gate of the switch units 221, 222, 223 is connected to the input terminal of the buffer unit and each p-type control gate of the switch units 221, 222, 223 is connected to the output terminal of the buffer unit 231. The switch units 221, 222, 223 are connected to the video signal lines 211, 212, 213 respectively. The scanning signals control the output of the plurality of switch units 221, 222, 223. With the arrival of the scanning signals, the plurality of switch units 221, 222, 223 output the video signals to data lines (not shown) in the active area (display area) 241 through output video signal lines 2211, 2221, 2231.

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Fig. 2b shows a schematic diagram of the parasitic capacitances associated with the first embodiment of the present invention. The parasitic